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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10069592	FILING DATE 07/16/2002	CLASS 174	SUBCLASS 110R	GAU 2831	EXAMINER MAYO
**APPLICANTS: Ajiki Kenichi;					
**CONTINUING DATA VERIFIED: This application is a 371 of PCT/JP01/05628 06/29/2001					
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** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-196088 06/29/2000					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 2002_0229A	
Verified and Acknowledged Examiners's initials					
TITLE : Covered conductor, soldering method using the covered conductor, and electric acoustic converter					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Applicati n Examiner	
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